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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/764,242	01/19/2001	George Wong	CS98-070B	8930
7:	590 06/13/2002			
George O. Saile			EXAMINER	
20 McIntosh Drive Poughkeepsie, NY 12603			DIAZ, JOSE R	
r oug.mospore,			ART UNIT	PAPER NUMBER
			2815	
			DATE MAILED: 06/13/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

•	Application N .	plicant(s)	Um
	09/764,242	WONG, GEORGE	
Office Action Summary	Examiner	Art Unit	
	José R. Díaz	2815	
The MAILING DATE of this communication app P riod for Reply	pears on the cover sheet with the	ne correspondence addres	:s
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1: after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply by within the statutory minimum of thirty (30) will apply and will expire SIX (6) MONTHS, cause the application to become ABAND	be timely filed) days will be considered timely. from the mailing date of this commu ONED (35 U.S.C. § 133).	nication.
Status			
1) Responsive to communication(s) filed on 14 I			
, <u> </u>	is action is non-final.		
3) Since this application is in condition for allows closed in accordance with the practice under Disposition of Claims			erits is
4) Claim(s) 18-22 is/are pending in the application	on.		
4a) Of the above claim(s) is/are withdraw	wn from consideration.		
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>18-22</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction and/o	r election requirement.		
Application Papers			
9)☐ The specification is objected to by the Examine	r.		
10)⊠ The drawing(s) filed on 19 January 2001 is/are:	a) accepted or b) objected	to by the Examiner.	
Applicant may not request that any objection to the	e drawing(s) be held in abeyance	. See 37 CFR 1.85(a).	
11)☐ The proposed drawing correction filed on	_ is: a)□ approved b)□ disap	proved by the Examiner.	
If approved, corrected drawings are required in rep	•		
12) ☐ The oath or declaration is objected to by the Ex	aminer.		
Priority under 35 U.S.C. §§ 119 and 120	•		
13) Acknowledgment is made of a claim for foreign	n priority under 35 U.S.C. § 11	9(a)-(d) or (f).	
a) ☐ All b) ☐ Some * c) ☐ None of:			
1. Certified copies of the priority documents	s have been received.		
2. Certified copies of the priority documents	s have been received in Appli	cation No	
Copies of the certified copies of the prior application from the International But See the attached detailed Office action for a list	reau (PCT Rule 17.2(a)).		je
14) Acknowledgment is made of a claim for domesti	c priority under 35 U.S.C. § 1	19(e) (to a provisional app	olication).
a) ☐ The translation of the foreign language pro 15)☒ Acknowledgment is made of a claim for domesti	visional application has been	received.	·
Attachment(s)			
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Inform	mary (PTO-413) Paper No(s) nal Patent Application (PTO-152 riginal drawings .	
S. Patent and Trademark Office PTO-326 (Rev. 04-01) Office Ac	tion Summary	Part of Pap	er No. 7

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DETAILED ACTION

Drawings

➤ The drawings are objected to because Figures 1-2 are new drawings, which were not presented in the disclosure of the parent application. Applicant is reminded that a later application for a distinct or independent invention, carved out of a pending application and disclosing and claiming only subject matter disclosed in an earlier or parent application is known as a divisional application or "division." The divisional application should set forth only that portion of the earlier disclosure which is germane to the invention as claimed in the divisional application (see MPEP 201.06). The Figures 1 and 2 that Applicant attached to the last response filed on March 14, 2002 are not the same as the Figures 1 and 2 filed on the Application. See copy of the drawings provided in this communication.

Claim Rejections - 35 USC § 102

➤ The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in-
- (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or
- (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

➤ Claims 18 and 19 are rejected under 35 U.S.C. 102(e) as being anticipated by Saitou et al. (US Patent No. 5,739,546).

Regarding claim 18, Saitou et al. teach a semiconductor device (see cols, 1-8) comprising a multilevel structure (see col. 6, lines 61-67 and col. 7, lines 1-3), wherein each level is comprised of a patterned conducting layer (10) formed in areas in which integrated circuits will be formed (2) and a patterned fill layer (6) formed in kerf areas or scribe lines (3), an oxide layer (7), and an insulating layer (9) (see Figs. 1-2).

Regarding claim 19, Saitou et al. teach that said patterned conducting layer is a metal (see col. 3, lines 10-16 and 22-25).

Claim Rejections - 35 USC § 103

- ➤ The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- ➤ Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Saitou et al. (US Patent No. 5,739,546) in view of Lou (US Patent No. 5,759,906).

Regarding claim 20, Saitou et al. fails to teach that said patterned conducting layer is aluminum having a thickness of about 6000-10000 Å. However, Lou teaches that it is well known in the art to form an aluminum of about 6000 Å as the patterned conducting layer in the areas in which integrated circuits will be formed (see col. 5, lines 34-39 and Figs. 3-11). Therefore, it would have been obvious to one having ordinary

skill in the art at the same time the invention was made to modify Saitou et al. to include a patterned conducting layer of aluminum having a thickness of about 6000-10000 Å. The ordinary artisan would have been motivated to modify Saitou et al. in the manner described above for at least the purpose of providing a high electrical conductivity.

> Claims 21-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Saitou et al. (US Patent No. 5,739,546).

Regarding claim 21, Saitou et al. teach that is well known in the art to form the patterned fill layer (6) close to the patterned conducting layer (10, 11), and wherein such layers are or are not separated by a space (see Figs. 1-3). Therefore, it would have been obvious to one having ordinary skill in the art at the same time the invention was made to modify Saitou et al. to include a space between the patterned fill layer and the patterned conducting layer of about 2 micrometers or less. The ordinary artisan would have been motivated to modify Saitou et al. in the manner described above for at least the purpose of achieving a high integration of chip regions within a wafer. Furthermore, it would have been obvious to one of ordinary skill in the art, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or working ranges involves only routine skill in the art. In re Aller, 105 USPQ 233.

Regarding claim 22, Saitou et al. fails to teach a width of about 120-600 micrometers. However, Saitou et al. teach that the width of the kerf area must be narrow in order to achieve a high integration of chip regions within a wafer (see col. 1, lines 29-31). Therefore, it would have been obvious to one having ordinary skill in the art at the Application/Control Number: 09/764,242

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same time the invention was made to modify Saitou et al. to include the limitation of narrowing the width of the kerf area to about 120-600 micrometers. The ordinary artisan would have been motivated to modify Saitou et al. in the manner described above for at least the purpose of achieving a high integration of chip regions within a wafer. Furthermore, it would have been obvious to one of ordinary skill in the art to provide a kerf area having a width of about 120-600 micrometers, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or working ranges involves only routine skill in the art. In re Aller, 105 USPQ 233.

Response to Arguments

> Applicant's arguments with respect to claims 18-22 have been considered but are most in view of the new ground(s) of rejection.

Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to José R. Díaz whose telephone number is (703) 308-6078. The examiner can normally be reached on 9:00 - 5:00 Monday, Tuesday, Thursday and Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (703) 308-1690. The fax phone numbers

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for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 746-3891 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

JRD June 10, 2002

> EDDIE LEE SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800

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